



Transcend's DDR5 DRAM modules operate at a nominal voltage of only 1.1V, offering higher energy efficiency and exceptional clock speeds to cater to the demands of the embedded industry. Power management integrated circuits (PMIC) provides better signal integrity and more stable power. All components are of the highest quality, having been sourced directly from the world's first-tier supplier of DRAM chips and stringently tested for unparalleled compatibility, reliability, and performance.

Features

- 1.1V low power supply
- · Power management IC (PMIC), high energy efficiency
- · On-die ECC
- Burst Length: 16, 32
- 16 bit pre-fetch
- Multi-Purpose Command (MPC)
- Decision Feedback Equalization (DFE)
- · SPD Hub with Thermal Sensor
- 100% tested for stability, compatibility and performance
- · Limited Lifetime Warranty

Advanced Technologies







30 μ" Gold Finger



DDR5 DIMMs





Module Type	DDR5 Long-DIMM	DDR5 SO-DIMM			
Standard	JEDEC® standard				
Speed	4800 MT/s				
Capacity	8GB~32GB				
Voltage	1.1V				
Pin Count	288 pin	262 pin			
PCB Height	1.23 inches	1.18 inches			
PCB Gold Finger Thickness	30μ" (ECC & Registered)				
Anti-Sulfuration	Default (ECC & Registered)				
Operating Temperature	Standard Temperature: 0°C~ 85°C				

Ordering Information

DDR5-4800

	Capacity	Component Composition	Rank x Org.	Long-DIMM	SO-DIMM
Unbuffered	8GB	(1Gx16)x4	1Rx16	TS1GLA64V8G	TS1GSA64V8G
	16GB	(2Gx8)x8	1Rx8	TS2GLA64V8E	TS2GSA64V8E
	32GB	(2Gx8)x16	2Rx8	TS4GLA64V8E	TS4GSA64V8E
ECC	16GB	(2Gx8)x10	1Rx8	TS2GLA72V8E	TS2GSA72V8E
	32GB	(2Gx8)x20	2Rx8	TS4GLA72V8E	TS4GSA72V8E
Registered	16GB	(2Gx8)x10	1Rx8	TS2GAR80V8E	-
	32GB	(2Gx8)x20	2Rx8	TS4GAR80V8E	-

Product specifications are subject to change without notice. Pictures shown may differ from actual products. Due to the complexity and variety of industrial applications, Transcend cannot guarantee 100% compatibility with all platforms and under all scenarios. For special applications and environments, it is strongly suggested that you contact Transcend beforehand for clarification.



